



Y0099-408 CIP

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#16
Formal Drawgs.
D. Smalls Logan
1-1303

In re patent application of

Cabral et al.

Serial No.: 09/902,483

Group Art Unit: 2813

Filing Date: July 11, 2001

Examiner: Erik Kielin

For: SELF-ALIGNED SILICIDE (SALICIDE) PROCESS FOR LOW RESISTIVITY
CONTACTS TO THIN FILM SILICON-ON-INSULATOR AND BULK MOSFETS
AND FOR SHALLOW JUNCTIONS

Assistant Commissioner of Patents
Washington, D.C. 20231

SUBMISSION OF CORRECTED FORMAL DRAWINGS

Sir:

Submitted herewith are corrected formal drawings for Figures 1-9B for the above-
identified patent application.

Approval and acknowledgment of receipt are respectfully requested.

Respectfully submitted,

Sean M. McGinn

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1/2/03

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